REMARKS

The office action cites the Amano reference under Section 103, suggesting that, based on Figure 1 and the Abstract, the filter bank 201 and decimation 202 corresponds to the claimed first digital decimation filter with n bands. It is further asserted that the filter bank 101 and decimation 102 correspond to a second digital decimation filter to reject n-1 bands coupled to the first decimation filter. However, the filter bank 201 and decimation 202 and the filter bank 101 and the decimation 102 are identical and, therefore, they could not meet the claimed limitations. Specifically, it is explained at column 1, lines 55-59, that the part 20 has the same structure as the part 10. It should be noted that the part 10 and the part 20 are the items that include the filter bank and decimation.
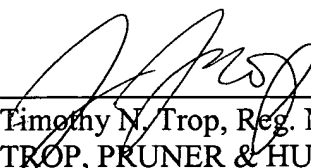
For example, in the embodiment illustrated in the specification at page 15, two cascaded digital decimation filters 142 and 144 are used. The filter 144 is called a multi-band digital decimation filter with n bands and the digital filter 142 is used to reject the n-1 multi-band of the system. For example, as shown in Figure 15, there are seven multi-bands 148. The filter 142 rejects six multi-bands 148b-148g, as shown in Figure 15, since only the leftmost band 148a and the multi-band digital decimation filter is of interest. As shown in Figure 16, the digital decimation rejection filter 142 has a pass band and stop band frequency with 53 taps of symmetry.

Thus, it is clear that because the two filters in the prior art are the same, they cannot accomplish what is set forth in claim 1.

Therefore, reconsideration is respectfully requested.

Respectfully submitted,

Date: <u>April 27, 2005</u>

Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Ste. 100
Houston, TX 77024
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation